

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chung-Kuan Cheng
Serial No.: 10/526,523
Conf. No.: 5310
Filed: 03/01/2005
For: INTERCONNECTION ARCHITECTURE AND
METHOD OF ASSESSING
INTERCONNECTION ARCHITECTURE
Art Unit: 2818
Examiner: Thinh T. Nguyen

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

TRANSMITTAL

Transmitted herewith in response to the Office letter dated April 18, 2008 is:

- (X) NOTICE OF APPEAL from the Examiner to the Board of Patent Appeals and Interferences
- (X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. 1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069.

Respectfully submitted,

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October 14, 2008
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